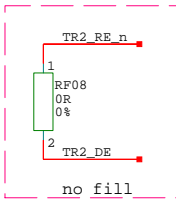


half-duplex flow control by DTR (PLX driver default)  
 driver may be permanently enabled if RF06 is de-populated



half-duplex only:  
 echo cancellation  
 remove RF05  
 populate RF08

close to pins 15/16

close to pins 9/10

fail safe bias resistors

fail safe bias resistors

1 & 2 on: half duplex  
 1 & 2 off: full duplex

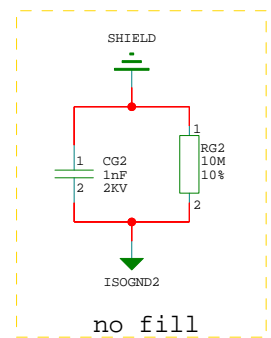
3 on: line termination A/B active  
 4 on: line termination Y/Z active

termination resistors size 1206  
 CT\*\* capacitors may be replaced by 0402 zero Ohm resistors for classic DC termination

mark component positions (silkscreen) RTA2 CTB2 CTZ2 RTY2 CG2 RG2

ESD protection TVS devices observe layout recommendations  
 TI slla292a.pdf page 8

noise reduction between ISO and SHIELD and frame ground



SER2

Board	DUI-MUSTANG	Rev.	0.0
Sheet Name	07: EIA 485 Transceiver 2	F	
Date, Design	2012-04-24	jj	Sheet 7 of 7